



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Ogawa

Serial No.: 09/964,995

Filed: September 27, 2001

Title: Semiconductor Device and  
Manufacturing Method Therefor

Attorney Docket No.: N29748500S

Group Art Unit: 2811

Examiner: Tran, Thien F.

#6  
Tracy Bell  
4-10-02

RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

The following is submitted in response to the Office Action dated February 20, 2002, and is currently due March 20, 2002.

REMARKS

Election without Traverse

In response to the restriction requirement set forth in the Office Action dated January 8, 2002, the claims of Group I (claims 1-11) are elected without traverse.

Respectfully Submitted,

Darryl G. Walker

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Attorney

Reg. No. 43,232

March 18, 2002

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37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:  
Assistant Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: March 18, 2002

Typed Name: Darryl G. Walker

Signature: Darryl G. Walker